

Mr. JAVEED BASHIR

Assistant Professor, Department of Electrical Engineering
Islamic University of Science and Technology Kashmir, Awantipora, Pulwama, J&K
e-mail: javeed.bashir@islamicuniversity.edu.in

Career Objective:

- To make my career with a professional and forward-looking organization where I can use my skills and abilities for my career as well as the organization.

Teaching and Research Experience:

- Total 8 plus years of teaching at various academic institutes in India.
 - Presently Working as Assistant Professor in the Department of Electrical Engineering, Islamic University of Science and Technology, Awantipora, Pulwama, J & K, since 7th April 2018 to till date.
 - Worked as contractual lecturer in the Department of Electrical Engineering, NIT Srinagar, since November 2010 to July 2012 and August 2014 to April 2015.
 - Worked as Junior Engineer and Assistant Engineer in JKPDD from April 2015 to May 2018.
 - Presented a guest lecture on “Applications of Neural Networks” in CMR Institute of Technology Bangalore

Educational/Professional Qualification:

- M.Tech. in Power System Engineering from IIT Roorkee, in 2014 with CGPA of 9.152/10.
- B.Tech. in Electrical Engineering from NIT Srinagar in 2010 with CPI of 8.88/10.

(RESEARCH, PUBLICATIONS and PROJECTS)

- Co-Principal Investigator of the project: “Design and implementation of reduced switch count multi-level inverter for unified power flow controller solar intermittency and power quality”, JKSTIC, DST, J&K, INR 2,74,000 for Two Year (Ongoing).
- J. Bashir and P. Jena, " Positive Sequence Superimposed based Islanding Detection Method used in Distributed Generation", *Distributed Generation & Alternative Energy Journal*, Vol 8, issue 02, pages 367-392, Jan 2023.
- J. Bashir and P. Jena, “A Novel Islanding Detection Method in a Distributed Generation Using Change in Phase Angle Difference Between Positive Sequence Current and Voltage.” *Transactions of the Indian National Academy of Engineering*, Vol 8, Issue 1, page 1-11, March-2023.

NATIONALCONFERENCE [04]

1. **J. Bashir**, P. Jena, and A. K. Pradhan, "Islanding detection of a distributed generation system using angle between negative sequence voltage and current," 2014 Eighteenth National Power Systems Conference (NPSC), Guwahati, India, 2014, pp. 1-5, doi: 10.1109/NPSC.2014.7103855.
2. S. Ahmad, A. S. Anees and **J. Bashir**, "Design and Development of a High Gain DC-DC Converter Suitable for Renewable Energy Application," 2023 International Conference on Power, Instrumentation, Energy and Control (PIECON), Aligarh, India, 2023, pp. 1-6, doi: 10.1109/PIECON56912.2023.10085871.
3. **J. Bashir**, M. U. Kanth, M. S. Aalam and P. Jena, " An innovative islanding detection scheme in distributed generation using rate of change of the ratio of negative and positive sequence superimposed voltage components," IEEE 7th International Conference on Computer Applications in Electrical Engineering-Recent Advances (CERA-2023), 27th to 29th October-2023 Roorkee India.
4. **J. Bashir**, M. U. Kanth, M. S. Aalam and P. Jena, " An Innovative Islanding Detection Scheme in a Distributed Generation using Phase Angle between Zero Sequence Superimposed components of current and voltage.," 2023 IEEE 3rd International Conference on Smart Technologies for Power, Energy and Control (STPEC), (10th to 13th December 2023) School of Electrical Engineering KIIT Deemed to be University, Bhubaneswar, India
5. **J. Bashir**, M. U. Kanth, M. S. Aalam and P. Jena, " An Innovative Islanding Detection Scheme in a Distributed Generation using Phase Angle between Zero Sequence Superimposed components of current and voltage.," 3rd international conference on Power Electronics, Smart Grid and Renewable Energy (PESGRE-2023), 17th to 20th December 2023, Kerala India.

(SDP/CONFERENCE / WORKSHOP / PRESENTATION)

Attended/Participated

- Attended short term Course on “Modelling and Simulation of Large Scale Systems”, organized by Department of Electrical Engineering, NIT Srinagar and Department of Electrical Engineering, IUST Awantipora during 13th April – 17th April 2015.
- Attended short term Course on “FPGA Programming in Power and Control Application”, organized by Department of Electrical Engineering, IUST Awantipora during 10th–14th September 2018.
- Attended short term Course on “Research Trends in Information Technology (RTIT-2018)”, organized by Department of Computer Science and Department of Computer Science Engineering, IUST Awantipora from 25th to 29th September, 2018.
- Attended IEEE workshop on “Recent Innovations in predictive control”, organized by Department of Electrical Engineering, IIT Bombay from 25th to 29th November, 2018.
- Attended two-day workshop on “Scientific and Technical Documentation using LaTeX”, organized by Department of Electrical Engineering, NIT Srinagar during 8th to 9th June 2019.
- Completed four MOOCS from SWAYAM portal from September 2020 to April 2021.
- Completed one month Faculty Induction Programme organized by UGC-HRDC Jamia Milia Islamia, New Delhi from 16-11-2020 to 15-12-2020
- Attended two-day workshop on “Computational Methods in Electrical Engineering”,

organized by Department of Electrical Engineering, IUST Awantipora during March 31-April 1, 2021.

- Organized a series of online webinars from the month of October to December and in coming months in the department of Electrical Engineering, IUST Awantipora.

UG/PG Projects:

- A new control scheme for regulating the voltage of an SMPS done in B. Tech.
- A new technique in Islanding Detection in Smart Grid environment done in M. Tech.

Achievement:

- **Qualified GATE** with gate score of 602 and AIR 886.
- **Qualified GATE** with gate score of 545 and AIR 1623.

Subjects Undertaken:

- Switch Gear and Protection.
- Advanced Power Systems.
- Power System Analysis.
- Basic Electrical Engineering.
- Electrical Machines.
- Flexible AC Transmission System (FACTS).
- Control Systems.
- Power Electronics.
- Electricity in Daily Use

Computer Skills:

- Languages C, C++.
- Operating systems Windows XP/98/2000...2011.
- Packages MS Office, **MATLAB/OPELRT, Power world and Mi Power, PS CAD/RSCAD/RTDS.**

Personal Vitae:

- Name: Mr. Javeed Bashir
- Father's Name: Mr. Bashir Ahmad Sheikh
- Date of birth: 20-06-1988
- Sex: Male
- Marital status: Married
- Languages known: English, Urdu, Hindi

Declaration: I hereby declare that above information is true according to my knowledge.

Date: 04/03/2024

Place: Awantipora

Mr. Javeed Bashir