

# Romana Yousuf

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## Education

- Pursuing PhD at NIT Srinagar
- M. Tech (Communication and Information Technology)  
Department of Electronics and Communication Engineering, NIT Srinagar
  - Dissertation: Design and Optimization of Carry Select Adder using FPGA
  - CGPA: 8.72 on a scale of 10
- B. E ( Electronics and Communication Engineering)  
MIET, University of Jammu
  - Percentage: 67%

## Experience

- **Assistant Professor** **September 2008 – Present**  
*Department of Electronics and Communication Engineering, Islamic University of Science and Technology, J&K*
  - I have been teaching courses related to Digital Design ó VLSI Design, DELD, Computer Organization, Embedded Systems, VHDL in addition to basic courses like Electronics, Electrical and Electronic Measurements. I have been intensively involved in setting up of labs for these courses.
  - Served as Incharge Head of the Department for one and half year
- **Project Faculty** **2007-2008**  
*SMDP Lab, Department of Electronics and Communication Engineering, NIT Srinagar*
- **Lecturer (on contract)** **2004-2005**  
*Department of Electronics and Communication Engineering, NIT Srinagar*

## Publications

- **Romana Yousuf**, Najeeb-ud-Din Shah óSynthesis of carry select adder in 65 nm FPGAö page no. 1-6, TENCON-2008, IEEE region 10 conference. E-ISBN: 978-1-4244-2409-2. Print ISBN 978-1-4244-2408-5.
- **Romana Yousuf**, Najeeb-ud-Din Shah óA novel carry select adder; topology and performance evaluation, 6th J&K Science Congress: 2010 ö
- **Romana Yousuf**, Jyoti Sharma, Najeeb-ud-Din Shah óSynthesis and relative performance of carry select and carry Skip Adder in 65 nm FPGAö International Conference on frontiers of computer science-2011
- Jawaaz Ahmad, **Romana Yousuf** óDouble Stacked Gates MOSFET Technology In both Planar as well as 3D Gate (FinFET) MOSFETø (Dual Stacked Gates In MOSFETø), International Journal of Electronics Communication and Computer Technology (IJECCT), Volume 5, Issue 6 (November 2015)
- Jawaaz Ahmad, **Romana Yousuf** óLight Dependent Resistor (LDR) Based Low Cost Light Intensity Measurement Circuit Design (LUX Meter)ö, International Journal of Innovative Research in Computer and Communication Engineering, Vol. 4, Issue 8, August 2016

- **Romana Yousuf** "SOLAR CELLS: The Ultimate Renewable Resource – A Review, Advanced Research in Electrical and Electronic Engineering p-ISSN: 2349-5804; e-ISSN: 2349-5812 Volume 3, Issue 5, October-December, 2016, pp. 440-448
- **Romana Yousuf** "Charge Sharing- A Review" Advanced Research in Electrical and Electronic Engineering, p-ISSN: 2349-5804; e-ISSN: 2349-5812 Volume 3, Issue 5, October-December, 2016, pp. 449-461
- **Romana Yousuf**, Najeeb-ud-Din Shah "Analysis of Charge Sharing in MOSFETs in view of available scaling trends", IOSR Journal of Electronics and Communication Engineering, Vol. 12, Issue 6, Ver. 2, Nov & Dec, 2017
- Umar Mohammad, Mohd Yusuf Yasin, **Romana Yousuf**, Imtiyaz Anwar "A novel square wave generator based on the translinear circuit scheme of second generation current controlled current conveyor-CCCII", 2019, SN Applied Sciences, Springer Nature Journal

### Books Published

- Design and Optimization of Carry Select Adders, Lambert Academic Publishing, 2018, ISBN: 978-613-9-94133-9

### Short-term courses/workshops attended

- "Nanotechnology: Present and Future Horizons" Department of Chemistry, N.I.T. Srinagar, 8th September, 2008.
- "Power Quality Problems and their Solutions" Department of Electrical Engineering, N.I.T. Srinagar, 7th -10th September, 2008
- Three day national workshop on Technical Paper Writing, Patent Drafting and Filing, Department of ECE, IUST Awantipora (12<sup>th</sup> to 14<sup>th</sup> December 2017)
- Two day workshop on Scientific and Technical Documentation using LATEX, Department of EE, IUST Awantipora (21<sup>st</sup> and 22<sup>nd</sup> December 2017)
- Two week ISTE STTP on Electric Power System, IIT Kharagpur (12<sup>th</sup> June to 15<sup>th</sup> July 2017)
- Attended three week Interdisciplinary Special Summer School/Refresher Course, UGC-HRD KU (20<sup>th</sup> July to 09<sup>th</sup> August 2017)
- Attended workshop on Preparation of Research Proposals to Funding Agencies, IUST Awantipora (26<sup>th</sup> & 28<sup>th</sup> September 2017)
- Attended one day International workshop on Cyber Communication: Security and Laws, Department of ECE, IUST Awantipora (8<sup>th</sup> May 2017)
- Attended three week Special Winter School/Refresher Course, UGC-ASC KU (10<sup>th</sup> Feb to 03rd March 2015)
- Attended three days Faculty Development Programme on Network Simulation and Related Technologies, using NetSIM software with DELLSOFT Technologies, Pvt, Ltd. (22<sup>nd</sup>-24<sup>th</sup> March 2017)
- Attended three days Faculty Development Programme on Power System Design and Analysis DigSILENT Power Factory software with DELLSOFT Technologies, Pvt, Ltd. (15<sup>th</sup> -17<sup>th</sup> March 2017)
- Attended one week Short Term Course on Materials Characterization Techniques from 24-28 June, 2019, NIT Srinagar.

## **Software Skills**

**Languages:** Assembly Language C-programming, VHDL.

**Softwares:** XILINX, Keil, PSPICE, SCAPS-1D, GPVDM

## DECLARATION

I hereby solemnly declare that the information provided is true to the best of my knowledge and belief

Romana Yousuf