



No. NIT/Elct / 14  
Dt. 1. 10. 2019

**NATIONAL INSTITUTE OF TECHNOLOGY SRINAGAR**  
Hazratbal Srinagar 190006 J&K India

**Title of the project: Model Order Reduction for Simulation Acceleration in Power Electronics**

**Name of the Principle Investigator: Dr. Mohammad Abid Bazaz**

**Names of the Co-PI: Dr. Shahkar Ahmad Nahvi**

**Project Outlay: Rs. 7.02 Lakhs**

**Certificate**

- A. This proposal has the approval of the Organization and all the existing facilities shall be made available for carrying out the studies on the proposed scheme
- B. That the research work proposed in the scheme does not in any way duplicate the research work already done or being carried out in the research station on the subject.
- C. That the project is not being partly or fully financed by grant from any other Organization / Government

**PI: Dr. Mohammad Abid Bazaz**

**HOD (EED) / Dean R&C**

**12-02-2019**

*Dean*  
Research and Consultancy  
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