

Department of Computer Science & Engineering



Date Sheet for Supplementary/End Term (Lateral Entry) Examination for B.Tech (CSE) 1ST, 3RD, 5TH Semesters.

Timing: - Semester 1ST: 10:30 a.m.-1:00 p.m. Semester 3RD & 5TH: 1:45 p.m.-4:15 p.m.

Note: Friday Timings : 10:30 a.m.-1:00p.m

SEMESTER	DATE/DAY	COURSE TITLE / COURSE CODE	Venue
3 rd SEMESTER	09-03-15/ Monday	Communication System-I CSE-301T	
	10-03-15/ Tuesday	Data Base Management System CSE-302T	
	11-03-15/ Wednesday	Digital Electronics and Logic Design CSE-303T	
	12-03-15/ Thursday	Data Structure CSE-304T	
	13-03-15/ Friday	Mathematics-III CSE-305T	
	16-03-15/Monday	Basic Electrical Engineering CSE-306T	
5 th SEMESTER	09-03-15/ Monday	Object Oriented Programming & System Design CSE-501T	
	10-03-15/ Tuesday	Software Engineering CSE-502T	
	11-03-15/ Wednesday	Microprocessor Peripherals and Interfacing CSE-503T	
	12-03-15/ Thursday	Algorithm Analysis & Design CSE-504T	
	13-03-15/ Friday	Mathematics-V (Numerical Methods) CSE-505T	
	16-03-15/Monday	Computer Organization & Architecture CSE-506T	

for Rapp
24/02/2015
Asst. I/C Exams CSE

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I/C Examination CSE

[Signature]
H.O.D CSE

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8. ACIT & e-Gov. for publishing on the website

No: IUST/CSE/Date Sheet/ *02*

Dated: 24/02/2015

Posted