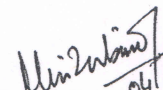



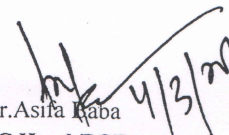
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
IUST, AWANTIPORA**

Supplementary/Special Date Sheet for B.Tech 5th & 7th Semester.

SEMESTER	DATE/DAY	COURSE TITLE (CODE)	TIMING	Venue
5 th SEMESTER	15-03-2016/ Tuesday	Computer Org. & Arch (ECE-501T)	1:40 p.m. – 4:10 p.m	ABII-103
	16-03-2016/ Wednesday	Antennas, Wave Propagation & Transmission Lines (ECE-502T)	1:40 p.m. – 4:10 p.m	
	17-03-2016/ Thursday	Microprocessors (ECE-503T)	1:40 p.m. – 4:10 p.m	
	18-03-2016/ Friday	Electrical Machines (ECE-504T)	1:40 p.m. – 4:10 p.m	
	22-03-2016/ Tuesday	VLSI Design (ECE-505T)	1:40 p.m. – 4:10 p.m	
	23-03-2016/ Wednesday	Data Structure (ECE-506T)	1:40 p.m. – 4:10 p.m	
7 th SEMESTER	15-03-2016/ Tuesday	Digital Signal Processing (ECE-701T)	1:40 p.m. – 4:10 p.m	ABII-107
	16-03-2016/ Wednesday	Data Communication (ECE-702T)	1:40 p.m. – 4:10 p.m	
	17-03-2016/ Thursday	Embedded System (ECE-703T)	1:40 p.m. – 4:10 p.m	
	18-03-2016/ Friday	Random Process & Noise (ECE-704T)	1:40 p.m. – 4:10 p.m	
	22-03-2016/ Tuesday	Power Systems (ECE-705T)	1:40 p.m. – 4:10 p.m	

Mr. 
Mir Zubair
Asst. Coordinator Exams
04/03/2016


Mr. Z.G. Khaki
Coordinator Exams
04/03/2016


Dr. Asifa Kaba
I/C Head ECE
4/3/2016

No: IUST/ECE/Duty Schedule/16/62
Dated: 04-03-2016

Copy to:

1. Dean SoT
2. Head (CSE, CE, ~~EE~~) **EE**
3. Deputy Registrar, Exams.
4. Deputy Registrar, Academics.
5. Notice Board
6. File.