

## NOTICE

It is for the information of all the student of CSE-4<sup>th</sup> Semester regular as well as re-registration/backlog candidates that the timing of their End Term / Re-registration / Backlog Exam for the Subject scheduled on 04-07-2016 (Monday) i.e. “Design and Analysis of Algorithm (CSE-404T)” will be **1:15 PM TO 3:45 PM** instead of 2:00 PM TO 4:30 PM. However there will be no change in the timing for rest of examination.

**I/C Examination**

**HOD (CSE)**

**Copy to:**

1. Dean SoT
2. I/C Controller Examination
3. DR Academics
4. Notice Board
5. File