

Department of Computer Science and Engineering

PRACTICAL DATE SHEET FOR END TERM EXAMINATION (Regular/Backlog)

B.TECH CSE 3rd, 5th & 7th SEMESTER (Autumn 2015)

Timing:-10:15 a.m. - 03:30 p.m.

Semester	Date/Day	Course Name (Course Code)	Venue
3 rd	01-01-2016 Friday	Communication System-I Lab	Concerned Labs
	04-01-2016 Monday	Basic Electrical Engineering Lab	
	05-01-2016 Tuesday	Digital Electronics and Logic Design lab	
	06-01-2016 Wednesday	Data Structure Lab	
	07-01-2016 Thursday	Data Base Management System Lab	
5 th	04-01-2016 Monday	Object Oriented Programming & System Design Lab	
	05-01-2016 Tuesday	Computer Networks Lab	
	06-01-2016 Wednesday	Microprocessor Peripherals and Interfacing Lab	
7 th	31-12-2015 (Thursday)	Artificial Neural Networks and Fuzzy logic Lab	
	01-01-2016 Friday	Web Technologies Lab	

Ms Saima Maqbool
Asst. I/C Exams CSE

Mr Sajad Ahmad Lone
I/C Examination CSE

Prof. A.G. Lone
H.O.D CSE

Copy to:

1. Dean SOT
2. H.O.D ENC, EE, CE
3. D.R Examination
4. D.R Academics
5. Concerned Teachers
6. Notice Board/File

No: IUST/CSE/Practical Date Sheet/

Dated: 15/12/2015