

Department of Computer Science and Engineering

**REREGISTRATION DATE SHEET FOR MINOR II EXAMINATION**

**B.TECH CSE 1<sup>st</sup>, 3<sup>rd</sup> & 5<sup>th</sup> SEMESTER (Autumn 2015)**

Semester	Date/Day	Timing	Course Name (Course Code)	Venue
<b>1<sup>st</sup>, 3<sup>rd</sup> &amp; 5<sup>th</sup></b>	<b>10-12-2015/ Thursday</b>	<b>10:15 p.m.- 11:15 p.m.</b>	<b>Electrical Measurement (CSE-304T)/ Mathematics –I (CSE-103T) /Object Oriented Programming and System Design (CSE-501T)</b>	<b>B1 Hut</b>
		<b>11:30 p.m. -12:30 p.m.</b>	<b>Mathematics –V (CSE- 505T)/ Mathematics- III (CSE-305T)</b>	
	<b>11-12-2015/ Friday</b>	<b>10:15 p.m.- 11:15 p.m.</b>	<b>Electronics-II (CSE-301T) Digital Electronics and Logic Design (CSE-303T)/ Data Structures (CSE-404T)</b>	
		<b>11:30 p.m. -12:30 p.m.</b>	<b>Algorithm Analysis and design (CSE-504T)/ Electronics Engineering Materials (CSE 303T)</b>	

Ms. Saima Maqbool  
Asst. I/C Exams CSE

Mr. Sajjad Ahmad Lone  
I/C Examination CSE

Prof. A.G. Lone  
H.O.D CSE

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No: IUST/CSE/Date Sheet/

Dated: 02/12/2015