

Department of Computer Science and Engineering  
**REREGISTRATION DATE SHEET FOR MINOR I EXAMINATION**  
 B.TECH CSE 1<sup>st</sup>, 3<sup>rd</sup> & 5<sup>th</sup> SEMESTER (Autumn 2015)

Semester	Date/Day	Timing	Course Name (Course Code)	Venue
<b>1<sup>st</sup>, 3<sup>rd</sup> &amp; 5<sup>th</sup></b>	<b>09-11-2015/ Monday</b>	10:30 p.m.- 11:30 p.m.	Electrical Measurement (CSE-304T)/ Mathematics –I (CSE-103T) /Object Oriented Programming and System Design (CSE-501T)	<b>Old computer lab CSE</b>
		02:00 p.m. -03:00 p.m.	Mathematics –V (CSE- 505T)/ Mathematics- III (CSE-305T)	
	<b>10-11-2015/ Tuesday</b>	10:30 p.m.- 11:30 p.m.	Network Analysis (CSE-302T)/ Electronics-II (CSE-301T) Digital Electronics and Logic Design (CSE-303T)/ Data Structures (CSE-404T)	
		02:00 p.m. -03:00 p.m.	Algorithm Analysis and design (CSE-504T)/ Electronics Engineering Materials (CSE 303T)	

Ms. Saima Maqbool  
Asst. I/C Exams CSE

Mr. Sajjad Ahmad Lone  
I/C Examination CSE

Prof. A.G. Lone  
H.O.D CSE

Copy to:

1. Dean SOT
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No: IUST/CSE/Date Sheet/

Dated: 05/11/2015