

Department of Computer Science and Engineering

DATE SHEET FOR END-TERM EXAMINATION (BACKLOG/RE-REGISTRATION OLD-CIRRICULUM)

B. TECH CSE 2nd & 4th SEMESTER (SPRING 2015)

Semester	Date/Day	Timing	Course Name (Course Code)	Venue
2nd	14-07-2015 (Tuesday)	01:30 p.m- 04:00p.m.	Electronics-I(CSE-202T)	B1 & B2 Hut
4th	16-07-2015 (Thursday)	01:30 p.m- 04:00p.m.	Operating System (CSE-406T)	
	22-07-2015 (Wednesday)	01:30 p.m- 04:00p.m.	Communication System –I (CSE-402T)	
	04-08-2015 (Tuesday)	10:00 a.m- 12:30 p.m.	Digital Electronics & Logic Design (CSE-401T)	
		01:30 p.m- 04:00p.m.	Data Structure (CSE-404T)	

Ms Saima Maqbool
Asst. I/C Exams CSE

Mr Sajjad Ahmad Lone
I/C Examination CSE

Prof. A.G. Lone
H.O.D CSE

Copy to:

1. Dean SOT
2. H.O.D ENC, EE, CE
3. D.R Examination
4. A.R Academics
5. I/C Examinations
6. Concerned Teachers
7. Notice Board for the information of students
8. File

No: IUST/CSE/Date Sheet/

Dated: 06/07/2015