

Department of Computer Science & Engineering
Date Sheet for Minor Examination & Labs (Lateral Entry; 3RDSem),
March 2015

Semester	Date/Day	Timing	Course Title (Course Code)	Venue
3RD	18-03-15/ Wednesday	10:00 a.m-11:30 a.m	Digital Electronics and Logic Design CSE-303T	Lab 1
		2:00 p.m.- 3:30 p.m.	Data Structure CSE-304T	
	19-03-15/ Thursday	10:00 a.m-11:30 a.m	Mathematics-III CSE-305T	
		2:00 p.m.- 3:30 p.m.	Basic Electrical Engineering CSE-306T	
	20-03-15 /Friday	10:00 a.m-11:30 a.m	Communication-I (CSE-301T)	
		2:00 p.m.- 3:30 p.m.	Data Base Management System(CSE302T)	
	23-03-2015/ Monday	10:30 a.m.-11:30 p.m	Data Base Management System Lab (CSE307-P)	Respective Labs
		12:00 O` Clock – 01:00 p.m.	Data Structure Lab(CSE 308- P)	
		2:00 p.m. -3:00 p.m.	Communication-I Lab(CSE-310- P)	
	24-03-2015/ Tuesday	10:30 a.m.-1:00 p.m	Digital Electronics and Logic Design Lab(CSE 309 - P)	
1:15 p.m. -3:45 p.m.		Basic Electrical Engineering Lab (CSE 311- P)		

Ms Saima Maqbool
Asst. I/C Exams CSE

Mr Sajjad Ahmad Lone
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Prof. A.G. Lone
H.O.D CSE

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No:
Dated: 17/03/2015