

Department of Computer Science & Engineering
Revised Date Sheet for Mid Term(I & II) Examination BTech 3rd Semester,
Sliding/Lateral Entry

SEMESTER	DATE/DAY	COURSE TITLE / COURSE CODE	TIMING
3rd SEMESTER	17-03-15/ Tuesday	Communication System-I CSE-301T	10:00a.m.-11:30 a.m.
		Data Base Management System CSE-302T	2:00 p.m. -3:30 p.m.
	18-03-15/ Wednesday	Digital Electronics and Logic Design CSE-303T	10:00a.m.-11:30 a.m.
		Data Structure CSE-304T	2:00 p.m. -3:30 p.m.
	19-03-15/ Thursday	Mathematics-III CSE-305T	10:00a.m.-11:30 a.m.
		Basic Electrical Engineering CSE-306T	2:00 p.m. -3:30 p.m.

Ms Saima Maqbool
Asst. I/C Exams CSE

Mr Sajjad Ahmad Lone
I/C Examination CSE

Prof. A.G. Lone
H.O.D CSE

Copy to:

1. Dean SOT
2. H.O.D ENC, EE, CE
3. D.R Examination
4. A.R Academics
5. I/C Examinations
6. Concerned Teachers
7. Notice Board for the information of students

No: IUST/CSE/Date Sheet/

Dated: 05/03/2015